200Gb/s QSFP-DD LR4 10km Optical Transceiver WT-DQ4FNL-N00

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1. Product Overview

General Description

This product is a 200Gb/s transceiver module designed for 10km optical communication applications. The design is compliant to 200G LR4 10km optical interface specification. The module converts 4 inputs channels (ch) of 50Gb/s (PAM4) electrical data to 4 LWDM optical signals, and multiplexes them into a single channel for 200Gb/s(PAM4) optical transmission. Reversely, on the receiver side, the module optically de-multiplexes a 200Gb/s(PAM4) input into 4 LWDM channels signals, and converts them to 4 channel output electrical data.

The central wavelengths of the 4 LWDM channels. It contains a duplex LC connector for the optical interface and a 76-pin connector for the electrical interface. To minimize the optical dispersion in the long-haul system, single-mode fiber (SMF) has to be applied in this module. Host FEC is required to support up to 10km fiber transmission.

The product is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP-DD Multi-Source Agreement (MSA). It has been designed to meet the harshest external operating conditions including temperature, humidity and EMI interference.

Functional Description

Features

- IEEE802.3bs compliant
- QSFP-DD MSA compliant
- 4 LWDM lanes MUX/DEMUX design
- Supports 212.5Gb/s aggregate bit rate
- Up to 10km transmission on single mode fiber (SMF) with FEC
- Operating case temperature: 0 to 70oC
- 200GAUI-4 electrical interface
- Maximum power consumption 10.8W
- LC duplex connector
- RoHS compliant

Applications

- Data Center Interconnect
- 200G Ethernet
- Enterprise Networking

2. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	TS	-40	85	degC	
Operating Case Temperature	TOP	0	70	degC	
Power Supply Voltage	VCC	-0.5	3.6	V	
Relative Humidity (non-condensation)	RH	0	85	%	

Parameter	Symbol	Min	Max	Units	Notes
Damage Threshold, each Lane	THd	3.5		dBm	

3. Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Units	Notes
Operating Case Temperature	TOP	0		70	degC	
Power Supply Voltage	VCC	3.135	3.3	3.465	V	
Data Rate, each Lane			26.5625		GBd	
Data Rate, each Lane			53.125		Gb/s	
Data Rate Accuracy		-100		100	ppm	
Pre-FEC Bit Error Ratio				2.4x10-4		
Post-FEC Bit Error Ratio				1x10-12		1
Control Input Voltage High		2		Vcc	V	
Control Input Voltage Low		0		0.8	V	
Link Distance with G.652	D	0.002		10	km	2

4. Recommended Operating Conditions

Parameter	Symbol	Min	Min	Typical	Max	Units	Notes
Wavelength Assignment	L0	1294.53	1294.53		1296.59	nm	
Wavelength Assignment	L1	1299.02	1299.02		1301.09	nm	
Wavelength Assignment	L2	1303.54	1303.54		1305.63	nm	
Wavelength Assignment	L3	1308.09	1308.09		1310.19	nm	
Transmitter	Transmitter	Transmitter	Transmitter	Transmitter	Transmitter	Transmitter	Transmitter
Data Rate, each Lane		26.5625 ±100 ppm	26.5625 ±100 ppm	26.5625 ±100 ppm	26.5625 ±100 ppm	GBd	
Modulation Format		PAM4	PAM4	PAM4	PAM4		
Side-mode Suppression Ratio	SMSR	30	30			dB	Modulated
Total Average Launch Power	PT				10.7	dBm	
Average Launch Power, each Lane	PAVG	-3.4	-3.4		5.3	dBm	1
Outer Optical Modulation Amplitude (OMAouter), each Lane	POMA	-0.4	-0.4		5.1	dBm	2

Parameter	Symbol	Min	Min	Typical	Max	Units	Notes
Launch Power in OMAouter minus TDECQ, each Lane		-1.8	-1.8			dBm	For ER ≥4.5dB
Launch Power in OMAouter minus TDECQ, each Lane		-1.7	-1.7			dBm	For ER <4.5dB
Transmitter and Dispersion Eye Clouser for PAM4, each Lane	TDECQ				3.2	dB	
Extinction Ratio	ER	3.5	3.5			dB	
Difference in Launch Power between any Two Lanes (OMAouter)					4	dB	
RIN16.5OMA	RIN				-132	dB/Hz	
Optical Return Loss Tolerance	TOL				15.6	dB	
Transmitter Reflectance	TR				-26	dB	
Average Launch Power of OFF Transmitter, each Lane	Poff				-30	dBm	
Receiver	Receiver	Receiver	Receiver	Receiver	Receiver	Receiver	Receiver
Data Rate, each Lane		26.5625 ± 100 ppm	26.5625 ± 100 ppm	26.5625 ± 100 ppm	26.5625 ± 100 ppm	GBd	
Modulation Format		PAM4	PAM4	PAM4	PAM4		
Damage Threshold, each Lane	THd	6.3				dBm	3
Average Receive Power, each Lane		-9.7			5.3	dBm	4
Receive Power (OMAouter), each Lane					5.1	dBm	
Difference in Receiver Power between any Two Lanes (OMAouter)					4.2	dB	
Receiver Sensitivity (OMAouter), each Lane	SEN				- max (- 7.2, SECQ -8.6)	dBm	For BER of 2.4E-4
Stressed Receiver Sensitivity (OMAouter), each Lane	SRS				-5.4	dBm	5
Receiver Reflectance	RR				-26	dB	
LOS Assert	LOSA	-25.7				dBm	
LOS De-assert	LOSD				-11.7	dBm	
LOS Hysteresis	LOSH	0.5				dB	
Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)	Stressed Conditions for Stress Receiver Sensitivity (Note 6)

Parameter	Symbol	Min	Min	Typical	Max	Units	Notes
Stressed Eye Closure for PAM4 (SECQ), Lane under Test		0.9			3.4	dB	
OMAouter of each Aggressor Lane			1.5	1.5		dBm	

5. Recommended Operating Conditions

Parameter	Test Point	Min	Typical	Max	Units	Notes
Power Consumption				10.8	W	
Supply Current	Icc				mA	
Transmitter (each Lane)	Transmitter (each Lane)	Transmitter (each Lane)	Transmitter (each Lane)	Transmitter (each Lane)	Transmitter (each Lane)	Transmitter (each Lane)
Overload Differential Voltage pk-pk	TP1a	900			mV	
Common Mode Voltage (Vcm)	TP1	-350		2850	mV	1
Differential Termination Resistance Mismatch	TP1			10	%	At 1MHz
Differential Return Loss (SDD11)	TP1			See CEI- 28G-VSR Equation 13-19	dB	
Common Mode to Differential conversion and Differential to Common Mode conversion (SDC11, SCD11)	TP1			See CEI- 28G-VSR Equation 13-20	dB	
Stressed Input Test	TP1a	See CEI- 28G-VSR Section				
		13.3.11.2.1				
Receiver (each Lane)	Receiver (each Lane)	Receiver (each Lane)	Receiver (each Lane)	Receiver (each Lane)	Receiver (each Lane)	Receiver (each Lane)
Differential Voltage, pk-pk	TP4			900	mV	
Common Mode Voltage (Vcm)	TP4	-350		2850	mV	1
Common Mode Noise, RMS	TP4			17.5	mV	
Differential Termination Resistance Mismatch	TP4			10	%	At 1MHz
Differential Return Loss (SDD22)	TP4			See CEI- 28G-VSR Equation 13-19	dB	
Common Mode to Differential conversion and Differential to Common Mode conversion (SDC22, SCD22)	TP4			See CEI- 28G-VSR Equation 13-21	dB	

Parameter	Test Point	Min	Typical	Max	Units	Notes
Common Mode Return Loss (SCC22)	TP4			-2	dB	2
Transition Time, 20 to 80%	TP4	9.5			ps	
Vertical Eye Closure (VEC)	TP4			5.5	dB	
Eye Width at 10-15 probability (EW15)	TP4	0.57			UI	
Eye Height at 10-15 probability (EH15)	TP4	228			mV	

6. Recommended Operating Conditions

Parameter	Symbol	Min	Max	Units	Notes
Temperature monitors absolute error	DMI_Temp	-3	3	degC	Over operating temperature range
Supply voltage monitor absolute error	DMI _VCC	-0.1	0.1	V	Over full operating range
Channel RX power monitor absolute error	DMI_RX_Ch	-3	3	dB	
Channel Bias current monitor	DMI_Ibias_Ch	-10%	10%	mA	
Channel TX power monitor absolute error	DMI_TX_Ch	-3	3	dB	

7. Pin Descriptions

Pin	Logic	Symbol	Description	Plug Sequence	Notes
1		GND	Ground	1B	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3B	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3B	
4		GND	Ground	1B	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3B	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3B	
7		GND	Ground	1B	1
8	LVTTL-I	ModSelL	Module Select	3B	
9	LVTTL-I	ResetL	Module Reset	3B	
10		VccRx	+3.3V Power Supply Receiver	2B	2
11	LVCMOS- I/O	SCL	2-wire serial interface clock	3B	
12	LVCMOS- I/O	SDA	2-wire serial interface data	3B	

Pin	Logic	Symbol	Description	Plug Sequence	Notes
13		GND	Ground	1B	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3B	
15	CML-O	Rx3n	Receiver Inverted Data Output	3B	
16	GND	Ground	1B		1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3B	
18	CML-O	Rx1n	Receiver Inverted Data Output	3B	
19		GND	Ground	1B	1
20		GND	Ground	1B	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3B	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3B	
23		GND	Ground	1B	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3B	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3B	
26		GND	Ground	1B	1
27	LVTTL-O	ModPrsL	Module Present	3B	
28	LVTTL-O	IntL	Interrupt	3B	
29		VccTx	+3.3V Power supply transmitter	2B	2
30		Vcc1	+3.3V Power supply	2B	2
31	LVTTL-I	InitMode	Initialization mode; In legacy QSFP applications, the InitMode pad is called LPMODE	3B	
32		GND	Ground	1B	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3B	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3B	
35		GND	Ground	1B	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3B	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3B	
38		GND	Ground	1B	1
39		GND	Ground	1A	1
40	CML-I	Tx6n	Transmitter Inverted Data Input	3A	

Pin	Logic	Symbol	Description	Plug Sequence	Notes
41	CML-I	Tx6p	Transmitter Non-Inverted Data Input	3A	
42		GND	Ground	1A	1
43	CML-I	Tx8n	Transmitter Inverted Data Input	3A	
44	CML-I	Tx8p	Transmitter Non-Inverted Data Input	3A	
45		GND	Ground	1A	1
46		Reserved	For future use	3A	3
47		VS1	Module Vendor Specific 1	3A	3
48		VccRx1	3.3V Power Supply	2A	2
49		VS2	Module Vendor Specific 2	3A	3
50		VS3	Module Vendor Specific 3	3A	3
51		GND	Ground	1A	1
52	CML-O	Rx7p	Receiver Non-Inverted Data Output	3A	
53	CML-O	Rx7n	Receiver Inverted Data Output	3A	
54		GND	Ground	1A	1
55	CML-O	Rx5p	Receiver Non-Inverted Data Output	3A	
56	CML-O	Rx5n	Receiver Inverted Data Output	3A	
57		GND	Ground	1A	1
58		GND	Ground	1A	1
59	CML-O	Rx6n	Receiver Inverted Data Output	3A	
60	CML-O	Rx6p	Receiver Non-Inverted Data Output	3A	
61		GND	Ground	1A	1
62	CML-O	Rx8n	Receiver Inverted Data Output	3A	
63	CML-O	Rx8p	Receiver Non-Inverted Data Output	3A	
64		GND	Ground	1A	1
65		NC	No Connect	3A	3
66		Reserved	For future use	3A	3
67		VccTx1	3.3V Power Supply	2A	2
68		Vcc2	3.3V Power Supply	2A	2
69		Reserved	For Future Use	3A	3
70		GND	Ground	1A	1

Pin	Logic	Symbol	Description	Plug Sequence	Notes
71	CML-I	Tx7p	Transmitter Non-Inverted Data Input	3A	
72	CML-I	Tx7n	Transmitter Inverted Data Input	3A	
73		GND	Ground	1A	1
74	CML-I	Tx5p	Transmitter Non-Inverted Data Input	3A	
75	CML-I	Tx5n	Transmitter Inverted Data Input	3A	
76		GND	Ground	1A	1

8. Technical Figure 1



9. Revision History

Version	Date	Description	Prepared by
V6	2025	Unified WincoTek datasheet format	WincoTek
V6.1	2026-10-23	Engineering and format review	WincoTek